

Abstracts

A new FET frequency multiplier

M. Jonsson, H. Zirath and K. Yhland. "A new FET frequency multiplier." 1998 MTT-S International Microwave Symposium Digest 98.3 (1998 Vol. III [MWSYM]): 1427-1430.

A new FET frequency multiplier is presented. The FET multiplier works in a similar way as a resistive mixer. The FET can be operated either as a series or a parallel switch. The multiplier has essentially no DC power dissipation and its stability properties are good, since the drain-to-source bias-voltage is zero. The multiplier is insensitive to device parameter variations. Different configurations are analysed. The preliminary measurements show a conversion loss of 4.5 dB. The authors believe that the conversion efficiency can be further improved.

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